

(Normally supplied with MZ1 Board)

Table of insulated wire links to be added to MZ1 Board for use in a typical small system.

Although the separate NIOREQ and NMREQ signals are not required unless use is made of the Z80 I/O ports, the necessary connexions have been included in the table below for completeness, and because the separate I/O ports can be used to advantage even in simple systems.

The main benefits of NREFSH, NBUSREQ, NBUSAK, and certain other Z80 signals occur in advanced, multiprocessor systems with dynamic memories, and these lines are not brought out to the bus in a typical small system.

Ident	FROM		Ident	TO		REMARKS
		Pin No			Pin No	
Ø	IC5	74S04 8	Ø	IC4	74LS74 3, 11	Ø CLOCK
H	IC6	MK3880 18	H	T1	BC177 base	NHALT
Q	IC6	MK3880 19	two wires Q	IC7	74LS75 4	NMREQ } Two wires
"	"	" "	X	IC11	74LS365 14	NMREQ }
X	IC6	MK3880 20	X	IC11	74LS365 2	NIOREQ
R	IC6	MK3880 21	R	IC11	74LS365 12	NRDS
T	IC6	MK3880 22	T	IC11	74LS365 4	NWDS
-	IC6	MK3880 23	-	-	-	NBUSAK
W	IC6	MK3880 24	W	IC4	74LS74 2, 9	NWAIT
-	IC6	MK3880 25	-	-	-	NBUSREQ
S	IC6	MK3880 26	S	IC1	74LS132 6	NRESET
M	IC6	MK3880 27	M	IC4	74LS74 12	M1
-	IC6	MK3880 28	(8) IC1	74LS132 9)	"	"
-	IC6	MK3880 28	-	-	-	-

'X' = Termination pads not labelled on kemitron's diagram 16/12/78 RevA for MZ1 circuit board

'-' = Signal not connected to bus